IN THE SPECIFICATION

Please amend the portions of the Specification identified below to read as indicated herein.

Please amend the title of the invention as follows:

DATA DETECTION IDENTIFYING A PATTERN IN PARALLELA DATA STREAMS

At page 4, between lines 8 and 9, please add the following paragraph

Fig. 2 is a block diagram of a testing unit.

At page 6, beginning at line 13, please add the following two paragraphs:

Figure 2 is a block diagram showing a testing unit 100 that includes a system 102, a comparator unit 125, and an analyzing unit 130. System 102 includes a multiplexing device 105, a deserializing unit 110, pattern recognition units 115A-E, and phase shifting units 120A-E.

Multiplexing device 105 receives a serial data stream at an input port 140. The serial data stream contains an identifier pattern. Deserializing unit 110 deserializes the identifier pattern and produces a deserialized identifier pattern. Multiplexing device 105 provides, at output ports 150A-E, a deserialized output that contains the deserialized identifier pattern. Pattern recognition units 115A-E are coupled to each one of the plurality of output ports 150A-E, recognize the deserialized identifier pattern corresponding to the identifier pattern within the serial data stream, and detect a phase of the deserialized identifier pattern. Phase shifting units 120A-E are coupled to each one of the plurality of output ports 150A-E and shift the phase of the output of each respective port 150A-E in correspondence with the detected phase of the deserialized

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identifier pattern. Comparator unit 125 compares the deserialized output corresponding to the known serial data sequence with an expected output signal. Analyzing unit 130 analyzes deviations of the deserialized output corresponding to the known serial data sequence with the expected output signal.